

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Y. OSONE et al  
Application No.: 09/943,512  
Filed: August 31, 2001  
For: MULTILAYER WIRING BOARD AND SEMICONDUCTOR  
DEVICE  
Art Unit: 2822  
Examiner: D. E. GRAYBILL **CONF. No. 8183**

**AMENDMENT**

**MS: Amendments**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

August 12, 2009

Sir:

In response to the Office Action, dated March 12, 2009, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

**Amendments to the Claims;**

**Amendments to the Drawings;**

**Remarks are included following the Amendments; and**

**An Appendix follows the Remarks and contains two (2) sheets of replacement drawings.**